

AMENDMENT TO THE SPECIFICATION

Please amend paragraph [0001] as shown.

Cross reference to related applications

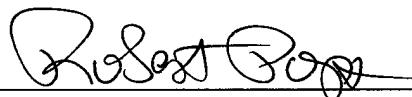
[0001] This application claims the benefit of U.S. provisional Patent Application Serial Number 60/398,943 filed July 25, 2002 for a "Modulation Doped Molecular-Scale Address Decoding" by Andre' DeHon, Patrick Lincoln, U.S. provisional Patent Application Serial Number 60/400,394 filed August 1, 2002 for a "Implementation of Computation Note 15: Integration Issues for Modulation Doped Memory" by Andre' DeHon, Patrick Lincoln, U.S. provisional Patent Application Serial Number 60/415,176 filed September 30, 2002 for "Nanoscale Architectures based on Modulation Doping" by Andre' DeHon, Patrick Lincoln, Charles Lieber, U.S. provisional Patent Application Serial Number 60/429,010 filed November 25, 2002 for "Stochastic Assembly of Sublithographic Nanoscale Interfaces" by Andre' DeHon, Patrick Lincoln, John E. Savage, U.S. provisional Patent Application Serial Number 60/441,995 filed January 23, 2003 for "Stochastic Assembly of Sublithographic Nanoscale Interfaces" by Andre' DeHon, Charles Lieber, Patrick Lincoln, U.S. provisional Patent Application Attorney Docket No. CIT-3877-P, serial number not yet assigned, filed April 25, 2003 for "Sublithographic Nanoscale 3D Architectures" by Andre' DeHon, and U.S. provisional Patent Application Attorney Docket No. CIT-3880-P, serial number not yet assigned, filed May 2, 2003 for "Computing with Electronic Nanotechnologies" by John E. Savage, Andre' DeHon, Patrick Lincoln, Lee-Ad Gottlieb, Arkady Yerukhimovich, the disclosure of all of which is incorporated herein by reference. Also incorporated by reference is the disclosure of U.S. Patent Application Attorney Docket No. 621116-2, serial number ~~not yet assigned~~ 10/627,406, filed on the same day of the present application for a "Sublithographic Nanoscale Memory Architecture" by John E. Savage, Andre' DeHon, Patrick Lincoln, and Charles Lieber.

The Commissioner is authorized to charge any additional fees which may be required or credit overpayment to deposit account no. 12-0415. In particular, if this response is not timely filed, then the Commissioner is authorized to treat this response as including a petition to extend the time period pursuant to 37 CFR 1.136 (a) requesting an extension of time of the number of months necessary to make this response timely filed and the petition fee due in connection therewith may be charged to deposit account no. 12-0415.

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as Priority mail in an envelope addressed to: Mail Stop RCE Commissioner for Patent P.O. Box 1450 Alexandria, VA 22313-1450 on

September 14, 2004
(Date of Deposit)
Robert Popa
(Name of Person Depositing)
Robert Popa
9/14/2004
Signature
Date

Respectfully submitted,



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